WHAT IS CLAIMED IS:

1. An output interface comprising:
an amplifier having an output impedance, wherein the amplifier sources a transmission line; and
a feed-forward circuit in parallel with said amplifier, wherein the feed-forward circuit compensates for transmission characteristics of the transmission line.

2. The output interface as in claim 1, wherein said feed-forward circuit further comprises a capacitor, wherein a capacitance value of said capacitor is determined at least in part by a data transition rate.

3. The output interface as in claim 1, wherein said feed-forward circuit further comprises a capacitor, wherein a capacitance value of said capacitor is determined based at least in part on a characteristic of a transmission medium to which said output interface is electrically coupled.

4. The output interface as in claim 1, wherein said feed-forward circuit further comprises an amplifier in series with a capacitor.

5. The output interface as in claim 1, wherein said feed-forward circuit further comprises a resistive element in series with a capacitor.

6. The output interface as in claim 1, wherein:
said feed-forward circuit further comprises a plurality of switched capacitors in parallel with each other, wherein each one of the switched capacitors

includes a capacitor in series with a switch and at least one of said plurality of switched capacitors is selectable based on a desired capacitance value to be placed in parallel with said output impedance.

7. The output interface as in claim 6, further comprises a feed-forward control module coupled to the feed-forward circuit to control a property of said feed-forward circuit based on at least one characteristic of a transmission medium to which said output interface is electrically coupled.

8. The output interface as in claim 7, wherein the feed-forward control module further comprises a plurality of user selectable switches.

9. The output interface as in claim 7, wherein the property is one of a capacitance value and a resistance value.

10. A device comprising:
a data processing module having an output;
an amplifier having an input coupled to the output of the data processor, and an output; and
a feed-forward circuit having an input coupled to the output of the data processing module and an output coupled to the output of the amplifier.

11 The device as in claim 10, wherein said feed-forward circuit further comprises a capacitor, wherein a capacitance value of said capacitor is determined at least in part by a data transition rate.

12. The device as in claim 10, wherein said feed-forward circuit further comprises a capacitor, wherein a

capacitance value of said capacitor is determined based at least in part on a characteristic of a transmission medium to which said output interface is electrically coupled.

13. The device as in claim 10, wherein said feed-forward circuit further comprises an amplifier in series with a capacitor.

14. The device as in claim 10, wherein said feed-forward circuit further comprises a resistive element in series with a capacitor.

15. The device as in claim 10, wherein:
said feed-forward circuit further comprises a plurality of switched capacitors in parallel with each other, wherein each one of the switched capacitors includes a capacitor in series with a switch and at least one of said plurality of switched capacitors is selectable based on a desired capacitance value to be placed in parallel with said output impedance.

16. The device as in claim 15, further comprises a feed-forward control module coupled to the feed-forward circuit to select a capacitance value of said feed-forward circuit based on at least one characteristic of a transmission medium to which said output interface is electrically coupled.

17. The device as in claim 16, wherein the feed-forward control module further comprises a plurality of user selectable switches.

18. A device comprising:
a printed circuit board;
a first device having an input;
a second device having an output:
an amplifier having an input and an output, wherein the input of the amplifier is coupled to an output of the second device and the output of the amplifier is coupled to the input of the first device via the printed circuit board; and
a feed-forward circuit in parallel with said amplifier.

19. The device as in claim 18, wherein said feed-forward circuit further comprises a capacitor, wherein a capacitance value of said capacitor is determined at least in part by a data transition rate.

20. The device as in claim 18, wherein said feed-forward circuit further comprises a capacitor, wherein a capacitance value of said capacitor is determined based at least in part on a characteristic of a transmission medium to which said output interface is electrically coupled.

21. The device as in claim 18, wherein said feed-forward circuit further comprises an amplifier in series with a capacitor.

22. The device as in claim 18, wherein said feed-forward circuit further comprises a resistive element in series with a capacitor.

23. The device as in claim 18, wherein:
said feed-forward circuit further comprises a plurality of switched capacitors in parallel with each other, wherein each one of the switched capacitors includes a capacitor in series with a switch and at least one of said plurality of switched capacitors is selectable based on a desired capacitance value to be placed in parallel with said output impedance.

24. The device as in claim 23, further comprising a feed-forward control module coupled to the feed-forward circuit to select a capacitance value of said feed-forward circuit based on at least one characteristic of a transmission medium to which said output interface is electrically coupled.

25. The device as in claim 24, wherein the feed-forward control module further comprises a plurality of user selectable switches.

26. A method comprising the steps of:
obtaining a capacitance indicator;
selecting a desired capacitance for a feed-forward circuit that is parallel with an amplifier, wherein the desired capacitance is based on the capacitance indicator.

27. The method of claim 26, wherein the step of obtaining further comprises reading the capacitance value from a specific location.

28. The method of claim 27, wherein the step of obtaining further comprises determining the capacitance value based on a data transmission rate.

29. The method of claim 27, wherein the step of obtaining further comprises determining the capacitance value based on an analysis of a transmitted signal.